

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

icant : Hongyong Zhang

Art Unit: 2812

Serial No.: 09/362,808

Examiner:

Filed

: July 28, 1999

Title

: METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Assistant Commissioner for Patents

Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims

30

20

\$180

Independent

6

\$234

TOTAL FEE DUE

A check for \$414 is attached.

\$414

Respectfully submitted,

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Harris

Reg. No. 32,030

SCH/smr

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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PRELIMINARY AMENDMENT

Sir:

Prior to initial examination, kindly amend the above identified divisional application as follows:

In the Claims:

Please amend claim 1 as follows:

1. (Amended) [A method of fabricating a thin-film transistor comprising a gate region having a gate electrode made of a material capable of being anodized and source-drain regions made of a semiconductor, said method comprising the steps of:

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01 FC:103 02 FC:102 180.00 OP 234.00 OP